

ISSUE CLASSIFICATION	
Class	Subject

PATENT NUMBER

<p>O.I.P.E.</p> <p>7H 11/1</p> <p>SCANNED Q.A. /pm</p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/057185	CONT/PRIOR D	CLASS -	SUBCLASS 3-76	ART UNIT	EXAMINER HA FAN
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Akira Nanba
Kenichi Ogura
Mamoru Ogino

375

2634

TITLE

Digital phase lock loop circuit

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) _____ (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)		ISSUE BATCH NUMBER	
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